

SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

PRODUCT SPECIFICATION

規格書

CUSTOMER: DATE:

(客戶): 志盛翔 (日期): 2018-08-23

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : GY 35V220μF(φ10X12.5)

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPL	IER
PREPARED (拟定)	CHECKED (审核)
孟庆庆	付婷婷

CUST	OMER
APPROVAL (批准)	SIGNATURE (签名)

ELECTROLYTIC CAPACITOR SPECIFICATION GY SERIES

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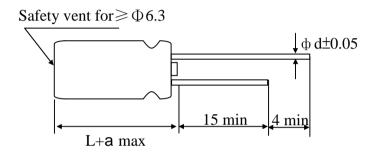
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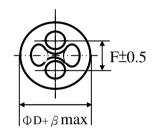
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Table 1 Product Dimensions and Characteristics

Unit: mm





а	L<20 : a=1.5; L≥20 : a=2.0
β	Φ D<20 : β =0.5; Φ D \geq 20 : β =1.0

* If it is flat rubber, there is no bulge from the flat rubber surface.

N	SAMXON	WV	Cap.	Com toloronoo	Temp.	tan δ (120Hz ,	Leakage	Max Ripple Current at 105 °C	Impedance at 20°C	Load lifeti		nsion nm)		Sleev
О.	Part No.	(Vdc)	(μF)	Cap. tolerance	range(°C)	(120HZ, 20°C)	Current (μA,2min)	100KHz (mA rms)	100kHz (Ωmax)	me (Hrs)	$D \times L$	F	фd	e
1	EGY227M1VG1BRR**P-R	35	220	-20% ~+20%	-40~105	0.12	77	580	0.16	7000	10X12.5	5.0	0.6	PET

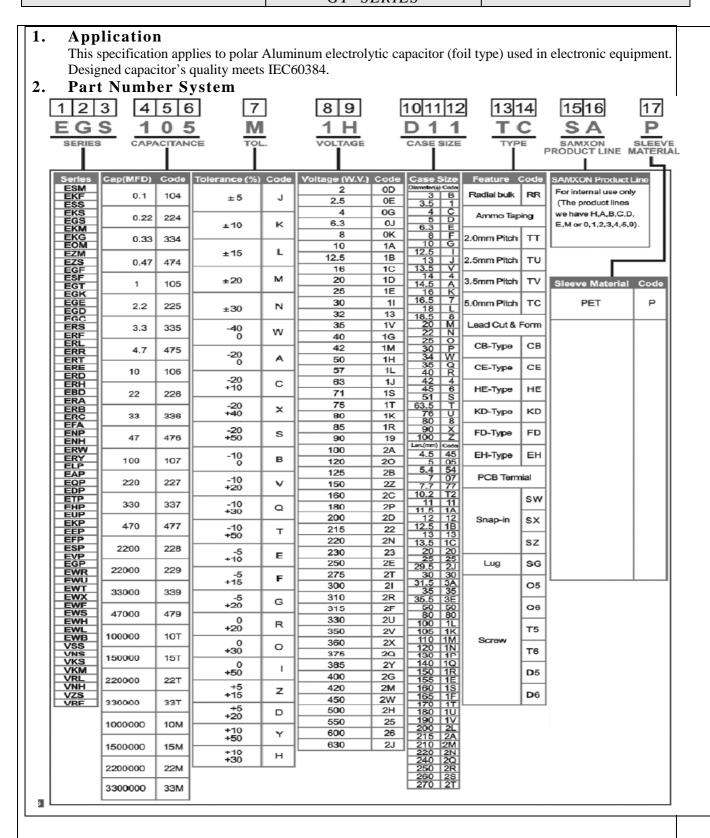
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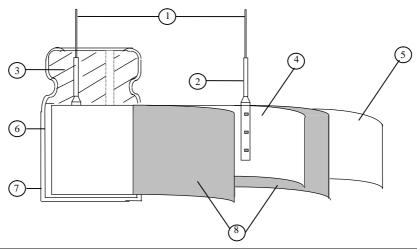


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3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

4. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C
Relative humidity : 45% to 85%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature $: 20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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	ITEM				PI	RFOR	MANC	E			
	Rated voltage	WV (V.DC)	6.3	10		16	25	35	50	63	100
	(WV)	SV (V.DC)	8	13		20	32	44	63	79	125
4.1		WW 415 6	1.50	200	220	2.50	2.50	100	120	450	
	Surge voltage (SV)	WV (V.DC)	160 200	200	220	300		400	420	450 500	
		SV (V.DC)	200	250	270	300	400	450	4/0	300	
4.2	Nominal capacitance (Tolerance)	<condition> Measuring F Measuring V Measuring T <criteria> Shall be with</criteria></condition>	requend oltage Cempera	: iture :	Not n 20±2	2°C	an 0.5V				
4.3	Leakage current	<condition> Connecting to minutes, and <criteria> Refer to Table</criteria></condition>	the capa					stor (1	k Ω ± 1	0Ω) in s	series for
4.4	tan δ	<condition> See 4.2, Nor <criteria> Refer to Tabl</criteria></condition>	m Capa	citance,	, for m	easurii	ng frequ	ency, vo	oltage ar	nd temper	ature.
4.5	Terminal strength		ength ocapacitor rength ocapacitor 2~3 sec	or, applie of Term r, applie onds, a ad wire less	inals. d forcend the	to bent Tensile (1	nt the ter	rminal (1	1~4 mm original Bendin (1 2.5	from the	rubber) f within 2-
		<criteri No notio</criteri 		hanges	shall l	e foun	d, no br	eakage (or loose	ness at the	e termin

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		<condition></condition>		•						
		STEP			$rature(^{\circ}\mathbb{C})$	_		Time		
		1		20 ± 2	<u>; </u>	Time	to reach t	thermal e	equilibriu	ım
		2	-40)(-25)	± 3	Time	to reach t	thermal e	equilibriu	ım
		3		20 ± 2	2	Time	to reach t	thermal e	equilibriu	ım
		4		105 ± 2	2	Time	to reach t	thermal e	equilibriu	ım
		5		20±2		_	to reach t			
		<criteria></criteria>							1	
		a. $tan \delta$ shall be	within th	he limi	it of Item	4.4The le	akage cu	rrent me	asured s	hall not
		more than 8 times					C			
	Temperature	b. In step 5, tan	δ shall b	e with	in the lin	it of Iter	n 4.4The	leakage	current	shall not
1.0	characteristi	more than the spe								
4.6	cs	c. At-40°C (-25°€	C), impe	dance	(z) ratio s	hall not e	exceed th	e value o	of the fol	lowing
		table.			1			ı	ı	1
		Working Voltage	(V)	6.3	10	16	25	35	50	63
		Z-25°C/Z+20°C	2	4	3	2	2	2	2	2
		Z-40°C/Z+20°C		8	6	4	3	3	3	3
		Working Voltage	(V) 1	100						
		Working Voltage (Z-25°C/Z+20°C	` '	2						
		Z-23 C/Z+20 C Z-40°C/Z+20°C		3						
					E 4 110	_	1 1000) E C	7.05/7.	20°C
		For capacitance v	arue > ro	ՍՍՍ Ա 1	r, Auu v	o der ano	mer rooo) µ F 10f	L- 23/L+	ZU (-).
		Capacitance, tan δ			Add 1.0	per anot	her 1000	μF for Z		
		Capacitance, tan δ			Add 1.0	per anot	her 1000	μF for Z		
			, and im	npedan	Add 1.0	per anot e measur	her 1000 ed at 120	μF for Z Hz.	Z-40°C/Z	Z+20℃.
		<condition></condition>	6, and im	npedan	Add 1.0 ce shall b	per anote measures, The ca	her 1000 ed at 120 pacitor is	μ F for Z Hz.	Z-40°C/Z	Z+20°C.
		<condition> According to IEC 105°C ±2 with D DC and ripple po</condition>	c60384-4 Cbias version of the control of the contro	No.4.1 Voltage	Add 1.0 ce shall b	b per anote e measur ds, The ca rated ripp	her 1000 ed at 120 pacitor is le current	μ F for Z Hz. s stored a t for Tab	Z-40°C/Z at a tempole 1 . (The voltage)	Z+20°C. erature of the sum of the the sum of the sum o
		<condition> According to IEC 105°C ±2 with D DC and ripple poproduct should be</condition>	C60384-4. OC bias verse voltage tested a	No.4.1 voltage age sh	Add 1.0 ce shall b	b per anote e measur ds, The ca rated ripp	her 1000 ed at 120 pacitor is le current	μ F for Z Hz. s stored a t for Tab	Z-40°C/Z at a tempole 1 . (The voltage)	Z+20°C. erature of the sum of the the sum of the sum o
	Load	<condition> According to IEC 105°C ±2 with D DC and ripple popoduct should be result should mee</condition>	C60384-4. OC bias verse voltage tested a	No.4.1 voltage age sh	Add 1.0 ce shall b	b per anote e measur ds, The ca rated ripp	her 1000 ed at 120 pacitor is le current	μ F for Z Hz. s stored a t for Tab	Z-40°C/Z at a tempole 1 . (The voltage)	Z+20°C. erature of the sum of the the sum of the sum o
4.7	life	Condition> According to IEC 105°C ±2 with D DC and ripple poroduct should be result should mee Criteria>	C60384-4 OC bias veak voltage tested a cet the foll	No.4.1 voltage age sh fter 16 lowing	Add 1.0 ce shall b	b per anote e measureds, The car eated ripp acceed the covering to	pacitor is le current e rated with a tate	μ F for Z Hz. s stored a t for Tab	Z-40°C/Z at a tempole 1 . (The voltage)	Z+20°C. erature of the sum of the the sum of the sum o
4.7		Condition> According to IEC 105°C ±2 with D DC and ripple poproduct should be result should mee Criteria> The characteristic	C60384-4 OC bias veak volta tested a cet the foll	No.4.1 voltage age sh fter 16 lowing	Add 1.0 ce shall be 13 method plus the reall not explosive table:	b per anote measures, The capated ripp acced the covering to grequire	pacitor is le current e rated with at att	μ F for Z Hz. s stored a t for Tab yorking v mospher	Z-40°C/Z at a tempole 1 . (The voltage)	Z+20°C. erature of the sum of the the sum of the sum o
4.7	life	Condition> According to IEC 105°C ±2 with D DC and ripple por product should be result should mee Criteria> The characteristic Leakage curves	C60384-4 DC bias verse voltage tested a cet the followers of the cet t	No.4.1 Voltage age sh fter 16 lowing	Add 1.0 ce shall be 13 method plus the reall not explain the real table: e following Value in	per anote measures, The carated ripper acced the covering to grequire 4.3 shall	pacitor is le current e rated wime at att	μ F for Z Hz. s stored a t for Tab corking v mospher	Z-40°C/Z at a tempole 1 . (The voltage)	Z+20°C. erature of the sum of the the sum of the sum o
4.7	life	Condition> According to IEC 105°C ±2 with D DC and ripple peroduct should be result should mee <criteria> The characteristic Leakage cu Capacitance</criteria>	C60384-4 DC bias verse voltage tested a cet the followers of the cet t	No.4.1 Voltage age sh fter 16 lowing	Add 1.0 ce shall be a shall not expluse the relation of the shall not expluse the shours recognized the shall be should be should be shall be sha	by per another measures, The care rated ripper acced the covering of the second shall and shall a 25% of	pacitor is le current e rated wime at attements. be satisfi initial va	μ F for Z Hz. s stored a t for Tab yorking v mospher ed llue.	Z-40°C/Z at a tempo ble 1 . (The voltage) ic condit	Z+20°C. erature of the sum of the the sum of the sum o
4.7	life	<condition> According to IEC 105°C ±2 with D DC and ripple poproduct should be result should mee <criteria> The characteristic Leakage cu Capacitance tan δ</criteria></condition>	C60384-4 DC bias veak voltage tested a cet the follower than the cet the follower than the cet the follower than the cet than generated the follower than the cet that the cet than	No.4.1 Voltage age sh fter 16 lowing	Add 1.0 ce shall be 13 method plus the reall not explain the real stable: e following Value in Within 1.0 Not more	b per anote measures, The carated ripp acceed the covering to	pacitor is le current e rated wrime at attements. be satisfi initial va	μ F for Z Hz. s stored a t for Tab corking v mospher ed due.	Z-40°C/Z at a tempole 1. (The voltage) ic condite the discondite	Z+20°C. erature of the sum of the the sum of the sum o
4.7	life	Condition> According to IEC 105°C ±2 with D DC and ripple peroduct should be result should mee <criteria> The characteristic Leakage cu Capacitance</criteria>	C60384-4 DC bias veak voltage tested a cet the follower than the cet the follower than the cet the follower than the cet than generated the follower than the cet that the cet than	No.4.1 Voltage age sh fter 16 lowing	Add 1.0 ce shall be a shall not expluse the relation of the shall not expluse the shours recognized the shall be should be should be shall be sha	b per anote measures, The carated ripp acceed the covering to	pacitor is le current e rated wrime at attements. be satisfi initial va	μ F for Z Hz. s stored a t for Tab corking v mospher ed due.	Z-40°C/Z at a tempole 1. (The voltage) ic condite the discondite	Z+20°C. erature one sum of then the
4.7	life	<condition> According to IEC 105°C ±2 with D DC and ripple peroduct should be result should mee <criteria> The characteristic Leakage cu Capacitance tan δ Appearance</criteria></condition>	C60384-4 DC bias veak voltage tested a cet the follower than the cet the follower than the cet the follower than the cet than generated the follower than the cet that the cet than	No.4.1 Voltage age sh fter 16 lowing	Add 1.0 ce shall be 13 method plus the reall not explain the real stable: e following Value in Within 1.0 Not more	b per anote measures, The carated ripp acceed the covering to	pacitor is le current e rated wrime at attements. be satisfi initial va	μ F for Z Hz. s stored a t for Tab corking v mospher ed due.	Z-40°C/Z at a tempole 1. (The voltage) ic condite the discondite	Z+20°C. erature of the sum of the the sum of the sum o
4.7	life	<condition> According to IEC 105°C ±2 with D DC and ripple poroduct should be result should mee <criteria> The characteristic Leakage cu Capacitand tan δ Appearanc <condition></condition></criteria></condition>	C60384-4. OC bias verse to the following control of the control of	No.4.1 Voltage age sh fter 16 lowing	Add 1.0 ce shall be 13 method plus the real not explain the real not exp	g require 4.3 shall 25% of than 200 all be no	pacitor is le current e rated wrime at attements. be satisficinitial value of the leakage of	μ F for Z Hz. s stored a t for Tab corking v mospher ed hue. s specifie of electro	Z-40°C/Z at a tempole 1. (The voltage) ic condite to discondite discondite to discondite to discondite to discondite to discondite	erature one sum of Then thions. Th
4.7	life	<condition> According to IEC 105°C ±2 with D DC and ripple poroduct should be result should mee <criteria> The characteristic Leakage or Capacitand tan δ Appearance <condition> The capacitors are</condition></criteria></condition>	C60384-4 DC bias veak voltae tested a et the follower the	No.4.1 voltage age sh fter 16 lowing ge	Add 1.0 ce shall be 13 method plus the reall not explain the real stable: e following Value in Within 1/2 Not more the shall no voltage.	b per anote measures, The caracted ripp acceed the covering to	pacitor is le current e rated wime at attiments. be satisfi initial valle akage o	μ F for Z Hz. s stored a t for Tab corking v mospher ed hue. s specifie of electro	Z-40°C/Z at a tempole 1. (The voltage) ic condite discondite disc	±2°C fo
4.7	life	Condition> According to IEC 105°C ±2 with D DC and ripple por product should be result should mee Criteria> The characteristic Leakage cu Capacitand tan δ Appearanc Condition> The capacitors are 1000+48/0 hours.	C60384-4 DC bias veak voltage tested a et the following the central material material central	No.4.1 voltage age sh fter 16 lowing aeet the	Add 1.0 ce shall be 13 method plus the relation of the shall not expense to the shall not expens	b per anote e measures, The caracted ripp acceed the covering to the covering	pacitor is le current e rated whime at attements. be satisfi initial valleakage of the leakage o	μ F for Z Hz. s stored a t for Tab corking v mospher ed llue. specifie of electro	Z-40°C/Z at a tempole 1. (The voltage) ic condite discondite disc	± 2°C form the test
4.7	life test	<condition> According to IEC 105°C ±2 with D DC and ripple poproduct should be result should mee <criteria> The characteristic Leakage cu Capacitand tan δ Appearanc <condition> The capacitors are 1000+48/0 hours, chamber and be a</condition></criteria></condition>	C60384-4 DC bias vertical and the tested a centre following the followin	No.4.1 voltage age sh fter 16 dowing aget the ge	Add 1.0 ce shall be 13 method plus the result all not expended to hours record table: The following within 1/2 Not more than the result as period to bilized at	g require 4.3 shall 25% of than 200 all be no	pacitor is le current e rated whime at attements. be satisficinitial various of the leakage of	μ F for Z Hz. s stored at for Tab yorking w mospher ed clue. specifie of electro mperatur be remo for 4~8	d value. de of 105 oved from hours.	±2°C for the test
4.7	life	Condition> According to IEC 105°C ±2 with D DC and ripple por product should be result should mee Criteria> The characteristic Leakage cu Capacitand tan δ Appearanc Condition> The capacitors are 1000+48/0 hours.	C60384-4 DC bias verther store change then store. Following allowed to a second control of the c	No.4.1 voltage age sh fter 16 lowing meet the	Add 1.0 ce shall be a specific to the result of the result	g require 4.3 shall 25% of than 200 all be no	pacitor is le current e rated whime at attribute satisficinitial various de data tentors shall apperature to ±100 Ω	μ F for Z Hz. s stored a t for Tab yorking v mospher ed hlue. s specifie of electro mperatur be rema for 4~8) with Γ	d value. lyte. e of 105 oved from hours. I	±2°C fon the test Next the d voltage
	life test	Condition> According to IEC 105°C ±2 with D DC and ripple por product should be result should mee Criteria> The characteristic Leakage cu Capacitand tan δ Appearanc Condition> The capacitors are 1000+48/0 hours. chamber and be a shall be connected	C60384-4 DC bias verther store change then store. Following allowed to a second control of the c	No.4.1 voltage age sh fter 16 lowing meet the	Add 1.0 ce shall be a specific to the result of the result	g require 4.3 shall 25% of than 200 all be no	pacitor is le current e rated whime at attribute satisficinitial various de data tentors shall apperature to ±100 Ω	μ F for Z Hz. s stored a t for Tab yorking v mospher ed hlue. s specifie of electro mperatur be rema for 4~8) with Γ	d value. lyte. e of 105 oved from hours. I	±2°C fon the test Next their
	life test Shelf life	<condition> According to IEC 105°C ±2 with D DC and ripple por product should be result should mee <criteria> The characteristic Leakage of Capacitand tan δ Appearance <condition> The capacitors are 1000+48/0 hours, chamber and be a shall be connected applied for 30min</condition></criteria></condition>	C60384-4 DC bias verther store change then store. Following allowed to a second control of the c	No.4.1 voltage age sh fter 16 lowing meet the	Add 1.0 ce shall be a specific to the result of the result	g require 4.3 shall 25% of than 200 all be no	pacitor is le current e rated whime at attribute satisficinitial various de data tentors shall apperature to ±100 Ω	μ F for Z Hz. s stored a t for Tab yorking v mospher ed hlue. s specifie of electro mperatur be rema for 4~8) with Γ	d value. lyte. e of 105 oved from hours. I	±2°C fon the test Next their
	life test Shelf life	<condition> According to IEC 105°C ±2 with D DC and ripple por product should be result should mee <criteria> The characteristic Leakage of Capacitand tan δ Appearance <condition> The capacitors are 1000+48/0 hours, chamber and be a shall be connected applied for 30min</condition></criteria></condition>	C60384-4 DC bias verther store change then store. Following allowed to a second control of the c	No.4.1 voltage age sh fter 16 lowing meet the	Add 1.0 ce shall be a specific to the result of the result	g require 4.3 shall 25% of than 200 all be no	pacitor is le current e rated whime at attribute satisficinitial various de data tentors shall apperature to ±100 Ω	μ F for Z Hz. s stored a t for Tab yorking v mospher ed hlue. specifie f electro mperatur be rema for 4~8) with Γ	d value. lyte. e of 105 oved from hours. I	±2°C fon the test Next the d voltage

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		<criteria></criteria>	the following requirements
		The characteristic shall meet Leakage current	Value in 4.3 shall be satisfied
	Shelf	Capacitance Change	Within ±25% of initial value.
4.8	life	tan δ	
	test		Not more than 200% of the specified value.
		Appearance	There shall be no leakage of electrolyte.
			stored more than 1 year, the leakage current may e through about 1 k Ω resistor, if necessary.
			e 15~35℃.
4.9	Surge	Leakage current	Not more than the specified value.
	test	Capacitance Change	Within $\pm 15\%$ of initial value.
		tan δ	Not more than the specified value.
		Appearance	There shall be no leakage of electrolyte.
		Attention: This test simulates over volta over voltage as often applied <condition></condition>	ge at abnormal situation only. It is not applicable to sucl
4.10	Vibration test	The following conditions sharperpendicular directions. Vibration frequency ra Peak to peak amplitude Sweep rate Mounting method: The capacitor with diameter g in place with a bracket. 4mm or les Criteria> After the test, the following in Inner construction Appearance	: 1.5mm : 10Hz ~ 55Hz ~ 10Hz in about 1 minute greater than 12.5mm or longer than 25mm must be fixed Within 30°

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		<condition></condition>					
		The capacitor shall be tes	ted under th	e following co	ondition	ıs:	
		Soldering temperature		: 245±3°C			
		Dipping depth		: 2mm			
4.11	Solderability	Dipping speed		: 25±2.5mm/s	S		
7.11	test	Dipping time : 3±0.5s					
		<c<u>riteria></c<u>					
		Coating quality		A minimum immersed	of 95%	of the surface	being
		<condition></condition>					
		Terminals of the capacito	r shall be in	nmersed into	solder	bath at 260±	5°Cfor10=
		1seconds or $400 \pm 10^{\circ}\text{C}$ for					
		Then the capacitor shall b					
	D :	for 1~2 hours before mea		the normal te	прегац	ne and norma	ii iiuiiiiuity
4.12	Resistance to solder heat	<criteria></criteria>	Burellielle.				
4.12	test	Leakage current	Not	more than the	e specifi	ied value.	
		Capacitance Change	Wit	hin ±10% of	initial	value.	
		tan δ		more than the	e specifi	ied value.	
		Appearance		re shall be no			e.
		FF					
		<condition></condition>					
		Temperature Cycle:Acco				ods, capacitor	shall be
		placed in an oven, the con		rding as belov		D:	
		Temperature				Time	
		(1)+20°C			€3	Minutes	
	Change of	(2)Rated low temper			30±2	Minutes	
4.13	temperature	(3)Rated high temper	rature (+105	°C)	30±2	Minutes	
	test	(1) to (3)=1 cycle, to	tal 5 cycle				
		<criteria></criteria>					
		The characteristic shall m					7
		Leakage current		re than the sp			
		tan 8		re than the sp			_
		Appearance	There s	hall be no lea	kage of	electrolyte.	
		<condition></condition>					
		Humidity Test:					
		According to IEC60384-4		-		-	
		hours in an atmosphere of		H .at $40\pm2^{\circ}$ C	the ch	aracteristic c	hange shall
		meet the following requir	rement.				
		<criteria></criteria>	N	.1 .1 .	C 1 1		
4.14	Damp heat	Leakage current		than the speci		ue.	
.,	test	Capacitance Change		20% of initial		· C· 1 1	
		tan δ		than 120% of			
		Appearance	There sha	ll be no leakag	ge ot ele	ectrolyte.	
İ							

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	The vent shall operate with no pieces of the capacitor and/or of		ous condi	itions suc	ch as flam	es or disp	ersion of
ıximum	The maximum permissible reat 120Hz and can be applied Table-1 The combined value of D.C.	d at maxi voltage	mum ope	erating te	mperatur	e	ceed the
missible	3.3~10	0.42	0.60	0.80	1.00		
	22~33	0.55	0.75	0.90	1.00		
irrent)							
r	eximum missible ripple arrent)	at 120Hz and can be applied Table-1 The combined value of D.C rated voltage and shall not a serious frequency Multipliers: Coefficient Cap. (µF) aximum missible ripple 22~33	The maximum permissible ripple cur at 120Hz and can be applied at maximum at 120Hz and can be applied at maximum at 120Hz and can be applied at maximum at 120Hz and shall not reverse vertically a simum at 120Hz and shall not reverse vertically a simum at 120Hz and shall not reverse vertically a simum at 120Hz and shall not reverse vertically a simum at 120Hz and shall not reverse vertically a simum at 120Hz and shall not reverse vertically a simum at 120Hz and shall not reverse vertically a simum at 120Hz and shall not reverse vertically a simum at 120Hz and can be applied at maximum at 120Hz and shall not reverse vertically a simum at 120Hz a	The maximum permissible ripple current is the at 120Hz and can be applied at maximum operated. Table-1 The combined value of D.C voltage and the prated voltage and shall not reverse voltage. Frequency Multipliers: Coefficient 120 1K Cap. (µF) 3.3~10 0.42 0.60 ripple ripple arrent) 47~330 0.70 0.85 470~1000 0.75 0.90	The maximum permissible ripple current is the maxim at 120Hz and can be applied at maximum operating te Table-1 The combined value of D.C voltage and the peak A.C rated voltage and shall not reverse voltage. Frequency Multipliers: Coefficient 120 1K 10K Cap. (μ F) 3.3~10 0.42 0.60 0.80 ripple ripple arrent) 47~330 0.70 0.85 0.95 470~1000 0.75 0.90 0.98	The maximum permissible ripple current is the maximum A.C coat 120Hz and can be applied at maximum operating temperature. Table-1 The combined value of D.C voltage and the peak A.C voltage strated voltage and shall not reverse voltage. Frequency Multipliers: Coefficient 120 1K 10K 100K Cap. (µF) 3.3~10 0.42 0.60 0.80 1.00 22~33 0.55 0.75 0.90 1.00 47~330 0.70 0.85 0.95 1.00 470~1000 0.75 0.90 0.98 1.00	The maximum permissible ripple current is the maximum A.C current at 120Hz and can be applied at maximum operating temperature Table-1 The combined value of D.C voltage and the peak A.C voltage shall not ex rated voltage and shall not reverse voltage. Frequency Multipliers: Coefficient 120 1K 10K 100K Cap. (µF) 3.3~10 0.42 0.60 0.80 1.00 22~33 0.55 0.75 0.90 1.00 47~330 0.70 0.85 0.95 1.00 470~1000 0.75 0.90 0.98 1.00

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances				
	Cadmium and cadmium compounds				
Heavy metals	Lead and lead compounds				
ricavy metais	Mercury and mercury compounds				
	Hexavalent chromium compounds				
	Polychlorinated biphenyls (PCB)				
Chloinated	Polychlorinated naphthalenes (PCN)				
organic	Polychlorinated terphenyls (PCT)				
compounds	Short-chain chlorinated paraffins(SCCP)				
	Other chlorinated organic compounds				
.	Polybrominated biphenyls (PBB)				
Brominated .	Polybrominated diphenylethers(PBDE) (including				
organic	decabromodiphenyl ether[DecaBDE])				
compounds	Other brominated organic compounds				
Tributyltin compo	ounds(TBT)				
Triphenyltin com	pounds(TPT)				
Asbestos					
Specific azo comp	pounds				
Formaldehyde					
Beryllium oxide					
Beryllium coppe	er				
Specific phthalate	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)				
Hydrofluorocarbo	on (HFC), Perfluorocarbon (PFC)				
Perfluorooctane s	ulfonates (PFOS)				
Specific Benzotri	azole				

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Attachment: Application Guidelines

1. Circuit Design

1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
 - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
 - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
 - a) At higher frequencies capacitance and impedance decrease while tanδ increases.
 - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

(1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

(2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

(3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

(4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

1.4 Using Two or More Capacitors in Series or Parallel

(1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

(2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

1.5 Capacitor Mounting Considerations

(1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

(2)Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

(3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

(4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ6.3~φ16mm:2mm minimum, φ18~φ35mm:3mm minimum, φ40mm or greater:5mm minimum.

(5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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(6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100° C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about $1k\Omega$.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately $1k\Omega$.
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result.

2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 °C for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150°C for a maximum time of 2 minutes.

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2.6 Capacitor Handling after Solder

- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60 °C maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinyl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100°C temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a 1000Ω , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

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